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(54) Title: SURFACE PLANARIZATION OF THIN SILICON FILMS DURING AND AFTER PROCESSING BY THE SEQUENTIAL LATERAL SOLIDIFICATION METHOD

(57) Abstract: Systems and methods for reducing a surface roughness of a polycrystalline or single crystal thin film produced by the sequential lateral solidification process are disclosed. In one arrangement, the system includes an excimer laser (110) for generating a plurality of excimer laser pulses of a predetermined fluence, an energy density modulator (120) for controllably modulating the fluence of the excimer laser pulses such that the fluence is below that which is required to completely melt the thin film, a beam homogenizer (144) for homogenizing modulated laser pulses in a predetermined plane, a sample stage (170) for receiving homogenized laser pulses to effect melting of portions of the polycrystalline or single crystal thin film corresponding to the laser pulses, translating means for controllably translating a relative position of the sample stage (170) with respect to the laser pulses, and a computer (110) for coordinating the excimer pulse generation and fluence modulation with the relative positions of the sample stage (170) to thereby process the polycrystalline or single crystal thin film by sequential translation of the sample stage (170) relative to the laser pulses.

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DESCRIPTION

SURFACE PLANARIZATION OF THIN SILICON FILMS DURING AND AFTER PROCESSING BY THE SEQUENTIAL LATERAL SOLIDIFICATION METHOD

NOTICE OF GOVERNMENT RIGHTS

The U.S. Government has certain rights in this invention pursuant to the terms of the Defense Advanced Research Project Agency award number N66001-98-1-8913.

BACKGROUND OF THE INVENTION

I. Field of the invention.

The present invention relates to techniques for semiconductor processing, and more particularly to semiconductor processing which may be performed at low temperatures.

II. Description of the related art.

In the field of semiconductor processing, there have been several attempts to use lasers to convert thin amorphous silicon films into polycrystalline films. An overview of conventional excimer laser annealing technology is presented by James Im et al. in "Crystalline Si Films for Integrated Active-Matrix Liquid-Crystal Displays," 11 MRS Bulletin 39 (1996). In systems used for carrying out excimer laser annealing, an excimer laser beam is shaped into a long beam which is typically up to 30 cm long and 500 micrometers or greater in width. The shaped beam is scanned over a sample of amorphous silicon to facilitate melting thereof and the formation of polycrystalline silicon upon resolidification of the sample.

The use of conventional excimer laser annealing technology to generate polycrystalline or single crystal silicon is problematic for several reasons. First, the silicon generated in the process is typically small grained, of a random microstructure, and/or has non-uniform grain sizes, which result in poor and non-uniform devices that lead to low manufacturing yield. Second, the processing

techniques needed to obtain acceptable performance levels require that the manufacturing throughput for producing polycrystalline silicon be kept low. Also, these processes generally require a controlled atmosphere and preheating of the amorphous silicon sample, which lead to a further reduction in throughput rates. Finally, the fabricated films generally exhibit an unacceptable degree of surface roughness that can be problematic for performance of microelectronic devices.

There exists a need in the field to generate higher quality polycrystalline silicon and single crystal silicon at greater throughput rates. As well, there exists a need for manufacturing techniques that reduce the surface roughness of such polycrystalline and single crystal silicon thin films to be used in the fabrication of higher quality devices, such as flat panel displays.

SUMMARY OF THE INVENTION

An object of the present invention is to provide techniques for planarizing the surfaces of polycrystalline and single crystal thin film semiconductors.

A further object of the present invention is to provide surface planarization techniques that may be applied as a post processing step to polycrystalline and single crystal thin film semiconductors that are produced during a sequential lateral solidification process.

Yet a further object of the present invention is to provide surface planarization techniques that may be applied as a processing step during the production of polycrystalline and single crystal thin film semiconductors in a sequential lateral solidification process.

Yet another object of the present invention is to provide techniques for the fabrication of high quality semiconductors devices useful for fabricating displays and other products.

In order to achieve these objectives as well as others that will become apparent with reference to the following specification, the present invention provides systems and methods for reducing surface roughness of a polycrystalline or single crystal thin film that had previously been produced by the sequential lateral solidification process. In one arrangement, the system includes an excimer laser for

generating a plurality of excimer laser pulses of a predetermined fluence, an energy density modulator for controllably modulating the fluence of the excimer laser pulses such that the fluence is below that which is required to completely melt the thin film, a beam homogenizer for homogenizing modulated laser pulses in a predetermined plane, a sample stage for receiving homogenized laser pulses to effect partial melting of portions of the polycrystalline or single crystal thin film corresponding to the laser pulses, translating means for controllably translating a relative position of the sample stage with respect to the laser pulses, and a computer for coordinating the excimer pulse generation and fluence modulation with the relative positions of the sample stage to thereby process the polycrystalline or single crystal thin film by sequential translation of the sample stage relative to the laser pulses. The excimer laser is preferably an ultraviolet excimer laser for generating ultraviolet excimer laser pulses.

In one arrangement, the beam homogenizer is operable to shape laser pulses with a tophat profile in both the x and y directions. The energy density modulator is operable to attenuate fluence of the excimer laser pulses to approximately 25% to 75% of the full melt threshold of the polycrystalline or single crystal thin film.

The translating stage advantageously includes an X direction translation portion and a Y direction translation portion, each being coupled to the computer and to each other and permitting movement in two orthogonal directions that are perpendicular to a path formed by the laser pulses, and being controllable by the computer for controllably translating the sample in both of said translatable directions under control of said computer. Also, the beam homogenizer is operable to shape said laser pulses with a tophat profile in both the x and y directions, and the translating means is operable to translate the polycrystalline or single crystal thin film in two directions orthogonal to a direction of said laser pulses such that sequential homogenized laser pulses are incident on slightly overlapping regions of the polycrystalline or single crystal thin film in the two directions.

In an alternative arrangement, the present invention provides for systems and methods for processing an amorphous silicon thin film sample into a single or polycrystalline silicon thin film having a reduced surface roughness. In one arrangement, the method includes forming a rigid cap layer on an amorphous silicon

thin film sample having sufficient thickness to withstand contractions and expansions during melting and resolidification of the silicon thin film during the sequential lateral solidification process. The method also includes generating a sequence of excimer laser pulses; controllably modulating each excimer laser pulse in the sequence to a predetermined fluence; homogenizing each modulated laser pulse in the sequence in a predetermined plane; masking portions of each homogenized fluence controlled laser pulse in the sequence to generate a sequence of fluence controlled pulses of patterned beamlets, irradiating the amorphous silicon thin film sample with the sequence of fluence controlled patterned beamlets to effect melting of portions thereof; controllably sequentially translating the sample relative to each of said fluence controlled pulse of patterned beamlets to thereby process the amorphous silicon thin film sample into a single or polycrystalline silicon thin film having a reduced surface roughness; and removing said cap layer from the processed single or polycrystalline silicon thin film.

The accompanying drawings, which are incorporated and constitute part of this disclosure, illustrate a preferred embodiment of the invention and serve to explain the principles of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a functional diagram of a system for performing the sequential lateral solidification process preferred to implement a preferred process of the present invention;

Fig. 2 is a chart showing the surface profile of a typical film which has been processed by the sequential lateral solidification system of Fig. 1;

Fig. 3 is a functional diagram of a preferred system for planarizing the surface of a polycrystalline or single crystal thin film semiconductor produced during a sequential lateral solidification process in accordance with the present invention;

Figs. 4a and 4b are illustrative diagrams of a crystallized silicon film to be processed by the system of Fig. 3 using a narrow beam;

Fig. 5 is an illustrative diagram of a crystallized silicon film to be processed by the system of Fig. 3 using a wide beam;

Figs. 6-7 are charts showing the surface profile of a typical film before

and after processing by the system of Fig. 3;

Fig. 8 is an illustrative diagram of a cross section of a crystallized silicon film processed by the system of Fig. 1 in accordance with a second embodiment of the present invention;

5 Fig. 9 is a chart showing the surface profile of a typical film which has been processed in accordance with the second embodiment of the present invention.

Fig. 10 is a flow diagram illustrating the steps implemented in the system of Fig. 3 in accordance with the first embodiment of the present invention; and

10 Fig. 11 is a flow diagram illustrating steps implemented in the system of Fig. 1 in accordance with the second embodiment of the present invention.

DESCRIPTION OF PREFERRED EMBODIMENTS

The present invention provides techniques for planarizing the surfaces of polycrystalline and single crystal thin film semiconductors. In the preferred
15 embodiments, the surface planarization techniques are applied as a post processing step to polycrystalline and single crystal thin film semiconductors that are produced during a sequential lateral solidification process, or as a processing step during the production of polycrystalline and single crystal thin film semiconductors in a sequential lateral solidification process. Accordingly, in order to fully understand
20 those techniques, the sequential lateral solidification process must first be appreciated.

The sequential lateral solidification process is a technique for producing large grained silicon structures through small-scale unidirectional translation of a silicon sample in between sequential pulses emitted by an excimer
25 laser. As each pulse is absorbed by the sample, a small area of the sample is caused to melt completely and resolidify laterally into a crystal region produced by the preceding pulses of a pulse set.

A particularly advantageous sequential lateral solidification process and an apparatus to carry out that process are disclosed in our co-pending patent application serial number 09/390,537, filed September 3, 1999, entitled "Systems and
30 Methods using Sequential Lateral Solidification for Producing Single or Polycrystalline Silicon Thin Films at Low Temperatures," the disclosure of which is incorporated by reference herein. While the foregoing disclosure is made with

reference to the particular techniques described in our co-pending patent application, it should be understood that other sequential lateral solidification techniques could readily be adapted for use in the present invention.

With reference to Fig.1, our co-pending patent application describes as a preferred embodiment a system including excimer laser 110, energy density modulator 120 to rapidly change the energy density of laser beam 111, beam attenuation and shutter 130, optics 140, 141, 142 and 143, beam homogenizer 144, lens system 145, 146, 148, masking system 150, lens system 161, 162, 163, incident laser pulse 164, thin silicon film sample 170, sample translation stage 180, granite block 190, support system 191, 192, 193, 194, 195, 196, and managing computer 100 X and Y direction translation of the silicon sample 170 may be effected by either movement of a mask 710 within masking system 150 or by movement of the sample translation stage 180 under the direction of computer 100.

As described in further detail in our co-pending application, an amorphous silicon thin film sample is processed into a single or polycrystalline silicon thin film by generating a plurality of excimer laser pulses of a predetermined fluence, controllably modulating the fluence of the excimer laser pulses, homogenizing the modulated laser pulses in a predetermined plane, masking portions of the homogenized modulated laser pulses into patterned beamlets, irradiating an amorphous silicon thin film sample with the patterned beamlets to effect melting of portions thereof corresponding to the beamlets, and controllably translating the sample with respect to the patterned beamlets and with respect to the controlled modulation to thereby process the amorphous silicon thin film sample into a single or polycrystalline silicon thin film by sequential translation of the sample relative to the patterned beamlets and irradiation of the sample by patterned beamlets of varying fluence at corresponding sequential locations thereon.

While the sequential lateral solidification process is highly advantageous to produce single crystal or large grained polycrystalline silicon thin films, the produced crystals often exhibit a surface roughness due to the irrative nature of the melting and resolidification inherent in the crystal growth process. Thus, as shown in Fig. 2, a 200 nm thick crystal will exhibit variations in height throughout the length of the crystal. In Fig. 2, a height of 0 indicates the optimal height in a 200 nm

thick crystal, and heights varying from 175 to 225 nm are shown to be common throughout the length of the crystal. Note the large bump 210 near the crystal boundary, where crystal thickness exceeds the optimal 200 nm thickness by 350 nm.

Referring to Figs. 3 and 4, a first embodiment of the present invention will now be described. Fig. 3 illustrates a post processing system embodiment for planarizing polycrystalline and single crystal thin film semiconductors produced by the sequential lateral solidification process. The system includes an excimer laser 310, beam attenuator and shutter 320, reflecting plate 330, telescoping lenses 331, 332, reflecting plate 333, beam homogenizer 340, condensing lens 345, reflecting plate 347, field lens 350, sample 360, sample translation stage 370, optical table 380, and managing computer 300. A preferred laser 310, attenuator 320, telescoping lenses 332, 332, homogenizer 340, and sample translation stage 370 that is movable in two orthogonal directions are each described in the co-pending patent application serial number 09/390,537. The table 380 may be as described in that patent document, or may be an ordinary table. It is preferable that the homogenized beam 346 be shaped with a tophat profile in both the x and y directions, and essential that the beam energy density is below that required to completely melt the sample 360.

With reference to Figs. 4a and 4b, the sample 360 is shown in greater detail. Since the sample in this embodiment has already been processed, it already includes a large number of single crystal regions, shown illustratively as chevron shaped crystals 365. The homogenized beam 346 is shown incident upon a portion 361 of sample 360 to induce partial melting thereof.

For a 200 nm thick silicon thin film, the full melt threshold is approximately 600 mJ/cm². Thus, to induce sufficient partial melting of the portion 361, a beam 346 having an energy that is approximately 25% to 75% of the full melt threshold should be utilized. If the beam is more energetic, energy fluctuations inherent in excimer lasers create the possibility of causing a full melt of the sample region 361. If the beam is less energetic, the sample portion 361 will not melt sufficiently to satisfactorily planarize.

As shown in Fig. 4b, the sample 360 includes a silicon oxide base layer 400 and a silicon layer 410. In accordance with the present invention, the outer surface of silicon layer 410 is caused to melt to a depth 420. Upon resolidification,

the rough surface 430 is reformed in a more planarized manner.

While a single homogenized beam pulse having an energy that is approximately 25% to 75% of the full melt threshold is sufficient to induce partial melting of the region 361, it is preferred that multiple beam pulses are caused to irradiate every such region. Each subsequent beam pulse will induce partial melting of the region 361, which upon resolidification will exhibit a more planarized surface. Thus, the use of ten beam pulses per region 361 will produce a far smoother surface 430 than would the use of a single pulse.

Returning to Fig. 4a, the sample stage 370 is translated, under the control of computer 300, from right to left to cause the homogenized beam 346 to scan the sample 360 from left to right 450 on the top of sample 360. The stage 370 is then moved in an orthogonal direction (shown as the Y direction) to realign the sample at a new position 460, and translation in the opposite direction is begun 470. This processes is repeated until the entire surface of sample 360 has been scanned by the homogenized beam 346.

When the sample stage is translated in the Y direction, it may be advantageous to align the homogenized beam to slightly overlap a previously scanned region of the sample 360. Thus, if the region 361 is 1.2 x 1.2 cm, Y direction translation of 1.15 cm may be utilized to avoid edge effects caused by irregularities in the homogenized beam. Likewise, it is advantageous to cause a slight overlap with X-direction translation is being effected.

While the foregoing has been described with respect to a tophat profile square homogenized beam, beams of other shapes may be utilized. Thus, as shown in Fig. 5, a wide homogenized beam 500 which is sufficiently wide to eliminated the need for X direction translation may be utilized, with the benefit of necessitating less movement by the translation stage 360, and adoringly, greater throughput. Likewise, a beam that is shaped with a Gaussian profile in the X direction could be utilized if greater overlaps between X translations are performed.

As shown in Fig. 6-7, the results of the process described with reference to Figs. 3-4a are illustrated. The profile of a sample 360 fabricated in accordance with the sequential lateral solidification process is shown in Fig. 6a. The sample exhibits surface irregularities of +/- 25 nm from the optimal 200 nm height.

As shown in Fig. 6b, after post processing with a single laser pulse in accordance with the present invention, those surface irregularities are markedly reduced. These results are alternatively illustrated in Fig. 7, where it is shown >100% decrease in surface roughness caused by post processing in accordance with the invention herein.

5 Referring next to Fig. 8, a second embodiment of the present invention will now be described. In this embodiment, the surface of silicon thin film is kept planarized through the employment of a rigid cap layer during the sequential lateral solidification process. Thus, Fig. 8 shows a thin silicon sample formed of an approximately 50-200 nm thick amorphous silicon layer 810 deposited on a silicon
10 oxide base layer 820. The sample is capped with a thick second silicon oxide layer 820, approximately 2 microns thick, which is substantially rigid. The cap layer must be sufficiently thick to withstand the contractions and expansions during melting and resolidification of the silicon layer during the sequential lateral solidification process.

The sample with cap layer 830 are then used in place of sample 170
15 in the lateral solidification process, a complete description of which is contained in the above mentioned patent application serial number 09/390,537. After such processing, the cap layer 830 is removed from the sample by traditional wet or dry etching techniques. As shown in Fig. 9, the results of the process described with reference to Fig. 8 is illustrated.

20 Referring to Fig. 10, the steps executed by computer 300 to control both the sequential lateral solidification process of Fig. 1 and the surface planarization process implemented with respect to Fig. 3 will be described. The various electronics of the system are initialized 1000 by the computer 300 to initiate the process. A sample is then loaded onto the sample translation stage 1005. It should be noted that
25 such loading may be either manual or robotically implemented under the control of computer 300. Next, the sample is processed in accordance with the sequential lateral solidification process using the apparatus of Fig. 1 1010. The processed sample is positioned for planarization 1015. The various optical components of the system are focused 1020 if necessary. The laser is then stabilized 1025 to a desired
30 energy level and repetition rate, as needed to partially melt the sample in accordance with the teachings of the present invention. If necessary, the attenuation of the laser pulses is finely adjusted 1030.

Next, translation of the sample is commenced 1035 at a predetermined speed and in a predetermined direction, in accordance with the previously sequential lateral solidification processed regions of the sample. The shutter is opened 1040 to expose the sample to irradiation and accordingly, to commence the planarization process.

Sample translation and irradiation continues until planarization has been completed 1045, 105, at which time the computer closes the shutter and stops translation 1055, 1060. If other areas on the sample have been designated for planarization, the sample is repositioned 1065, 1066 and the process is repeated on the new area. If no further areas have been designated for planarization, the laser is shut off 1070, the hardware is shut down 1075, and the process is completed 1080.

Referring next to Fig. 11, the steps executed by computer 100 to control the crystal growth process with the surface planarization steps implemented with respect to Fig. 1 will be described. Fig. 10 is a flow diagram illustrating the basic steps implemented in the system of Fig. 1 using a capped sample as illustrated in Fig. 8. An oxide layer is deposited on a base 1100. A silicon layer is then deposited on the oxide buffer layer 1110, and a cap oxide is deposited at the top layer of the sample 1120.

Next, the sample is processed in accordance with the sequential lateral solidification process using the apparatus of Fig. 1 1030. After processing, the cap oxide is removed, e.g., by a dilute hydrofluoric acid solution.

The foregoing merely illustrates the principles of the invention. Various modifications and alterations to the described embodiments will be apparent to those skilled in the art in view of the teachings herein. For example, while removal of the cap layer had been disclosed with respect to use of a dilute hydrofluoric acid solution, the cap layer may be removed by any conventional technique such as dry etching. It will thus be appreciated that those skilled in the art will be able to devise numerous systems and methods which, although not explicitly shown or described herein, embody the principles of the invention and are thus within the spirit and scope of the invention.

CLAIMS

1. A system for reducing a surface roughness of a polycrystalline or single crystal thin film produced by the sequential lateral solidification process, comprising:
 - (a) an excimer laser for generating a plurality of excimer laser pulses of a predetermined fluence;
 - (b) an energy density modulator, optically coupled to said excimer laser, for controllably modulating said fluence of said excimer laser pulses emitted by said excimer laser such that said fluence is below that which is required to completely melt said polycrystalline or single crystal thin film;
 - (c) a beam homogenizer, optically coupled to said energy density modulator, for homogenizing said modulated laser pulses in a predetermined plane;
 - (d) a sample stage, optically coupled to said mask, for receiving said homogenized laser pulses to effect partial melting of portions of said polycrystalline or single crystal thin film corresponding to said laser pulses;
 - (f) translating means, coupled to said sample stage, for controllably translating a relative position of said sample stage with respect to said laser pulses; and
 - (g) a computer, coupled to said excimer laser, said energy density modulator, and said translating means, for controlling said controllable fluence modulation of said excimer laser pulses and said controllable relative positions of said sample stage and said laser pulses, and for coordinating said excimer pulse generation and said fluence modulation with said relative positions of said sample stage and said laser pulses, to thereby process said polycrystalline or single crystal thin film by sequential translation of said sample stage relative to said laser pulses at corresponding sequential locations thereon.

- 1 2. The system of claim 1, wherein said excimer laser is a ultraviolet excimer
2 laser for generating ultraviolet excimer laser pulses.
- 1 3. The system of claim 1, wherein said beam homoginizer is operable to shape
2 said laser pulses with a tophat profile in both the x and y directions.
- 1 4. The system of claim 1, wherein said an energy density modulator is operable
2 to attenuate said flucnce of said excimer laser pulses to approximately 25%
3 to 75% of the full melt threshold of said polycrystalline or single crystal thin
4 film.
- 1 5. The system of claim 1, wherein said translating means comprises said sample
2 stage, and wherein said sample stage includes a Y direction translation portion
3 coupled to said computer and permitting movement in one direction
4 orthogonal to a direction of said laser pulses and being controllable by said
5 computer for controllably translating said polycrystalline or single crystal thin
6 film in said translatable direction under control of said computer.
- 1 6. The system of claim 5, wherein said beam homoginizer is operable to shape
2 said laser pulses with a tophat profile in at least said direction orthogonal to
3 a direction of said laser pulses, and wherein said translating means is operable
4 to translate said polycrystalline or single crystal thin film in said direction
5 orthogonal to a direction of said laser pulses, such that sequential
6 homogenized laser pulses are incident on slightly overlapping regions of said
7 polycrystalline or single crystal thin film.
- 1 7. The system of claim 1, wherein said translating means comprises said sample
2 stage, and wherein said sample stage includes an X direction translation
3 portion and a Y direction translation portion, each being coupled to said
4 computer and to each other, said X and Y direction translation portions
5 permitting movement in two orthogonal directions that are perpendicular to

6 a path formed by said laser pulses and being controllable by said computer
7 for controllably translating said sample in both of said translatable directions
8 under control of said computer.

1 8. The system of claim 7, wherein said beam homogenizer is operable to shape
2 said laser pulses with a tophat profile in both the x and y directions, and
3 wherein said translating means is operable to translate said polycrystalline or
4 single crystal thin film in two directions orthogonal to a direction of said laser
5 pulses, such that sequential homogenized laser pulses are incident on slightly
6 overlapping regions of said polycrystalline or single crystal thin film in said
7 two directions.

1 9. A method system for reducing a surface roughness of a polycrystalline or
2 single crystal thin film produced by the sequential lateral solidification
3 process, comprising the steps of:
4 (a) generating a plurality of excimer laser pulses of a predetermined
5 fluence;
6 (b) controllably modulating said fluence of said excimer laser pulses
7 emitted by said excimer laser such that said fluence is below that
8 which is required to completely melt said polycrystalline or single
9 crystal thin film;
10 (c) homogenizing said modulated laser pulses in a predetermined plane;
11 (d) effecting partial melting of portions of said polycrystalline or single
12 crystal thin film corresponding to said laser pulses; and
13 (f) controllably translating a relative position of said sample stage with
14 respect to said laser pulses to process said polycrystalline or single
15 crystal thin film by sequential translation of said sample stage relative
16 to said laser pulses at corresponding sequential locations thereon.

1 10. The method of claim 9, wherein said excimer laser pulses comprise ultraviolet
2 excimer laser pulses.

- 1 11. The method of claim 8, wherein said homogenizing step comprises
2 homogenizing said laser pulses with a tophat profile in both the x and y
3 directions.
- 1 12. The method of claim 8, wherein said modulating step comprises attenuating
2 said fluence of said excimer laser pulses to approximately 25% to 75% of the
3 full melt threshold of said polycrystalline or single crystal thin film.
- 1 13. The method of claim 8, wherein said translating step comprises controllably
2 translating said polycrystalline or single crystal thin film in one direction
3 orthogonal to a direction of said laser pulses.
- 1 14. The method of claim 13, wherein said homogenizing step comprises
2 homogenizing said laser pulses with a tophat profile in at least said direction
3 orthogonal to a direction of said laser pulses, and wherein said translating step
4 comprises translating said polycrystalline or single crystal thin film in said
5 direction orthogonal to a direction of said laser pulses, such that sequential
6 homogenized laser pulses are incident on slightly overlapping regions of said
7 polycrystalline or single crystal thin film.
- 1 15. The method of claim 8, wherein said translating step comprises controllably
2 translating said polycrystalline or single crystal thin film in two orthogonal
3 directions that are perpendicular to a path formed by said laser pulses.
- 1 16. The method of claim 15, wherein said homogenizing step comprises
2 homogenizing said laser pulses with a tophat profile in said two directions
3 orthogonal to a direction of said laser pulses, and wherein said translating
4 step comprises translating said polycrystalline or single crystal thin film in
5 said two directions such that sequential homogenized laser pulses are incident
6 on slightly overlapping regions of said polycrystalline or single crystal thin

7 film in said two directions.

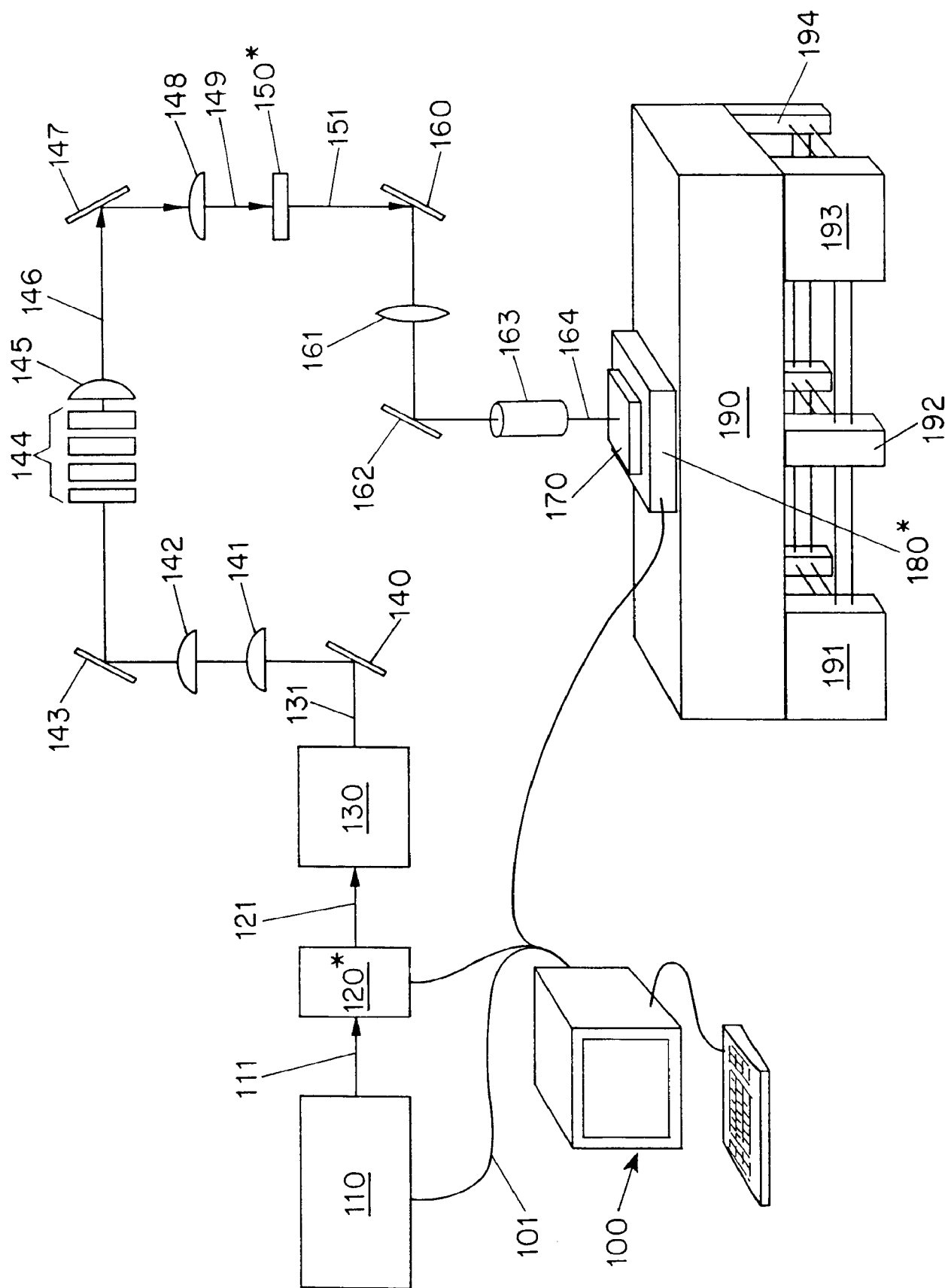
1 17. The method of claim 8, wherein said translating step comprises translating
2 said polycrystalline or single crystal thin film. after at least two of said beam
3 pulses irradiate said portion of said polycrystalline or single crystal thin film.

1 18. A method for processing an amorphous silicon thin film sample into a single
2 or polycrystalline silicon thin film having a reduced surface roughness ,
3 comprising the steps of:

- 4 (a) forming a rigid cap layer on said amorphous silicon thin film sample
5 having sufficient thickness to withstand contractions and expansions
6 during melting and resolidification of said silicon thin film;
7 (b) generating a sequence of excimer laser pulses;
8 (c) controllably modulating each excimer laser pulse in said sequence to
9 a predetermined fluence;
10 (d) homogenizing each modulated laser pulse in said sequence in a
11 predetermined plane;
12 (e) masking portions of each homogenized fluence controlled laser pulse
13 in said sequence to generate a sequence of fluence controlled pulses
14 of patterned beamlets,
15 (f) irradiating said amorphous silicon thin film sample with said sequence
16 of fluence controlled patterned beamlets to effect melting of portions
17 thereof corresponding to each fluence controlled patterned beamlet
18 pulse in said sequence of pulses of patterned beamlets;
19 (g) controllably sequentially translating said sample relative to each of
20 said fluence controlled pulse of patterned beamlets to thereby process
21 said amorphous silicon thin film sample into a single or
22 polycrystalline silicon thin film; and
23 (h) removing said cap layer from said single or polycrystalline silicon thin
24 film.

- 1 19. The method of claim 18, wherein said excimer laser pulses comprise
2 ultraviolet excimer laser pulses.
- 1 20. The method of claim 18, wherein said step of forming a rigid cap layer on said
2 amorphous silicon thin film sample comprises forming a silicon oxide layer
3 on said amorphous silicon thin film sample.
- 1 21. The method of claim 18, wherein said step of forming a rigid cap layer on said
2 amorphous silicon thin film sample comprises forming a silicon oxide layer
3 approximately 2 microns thick on said amorphous silicon thin film sample.

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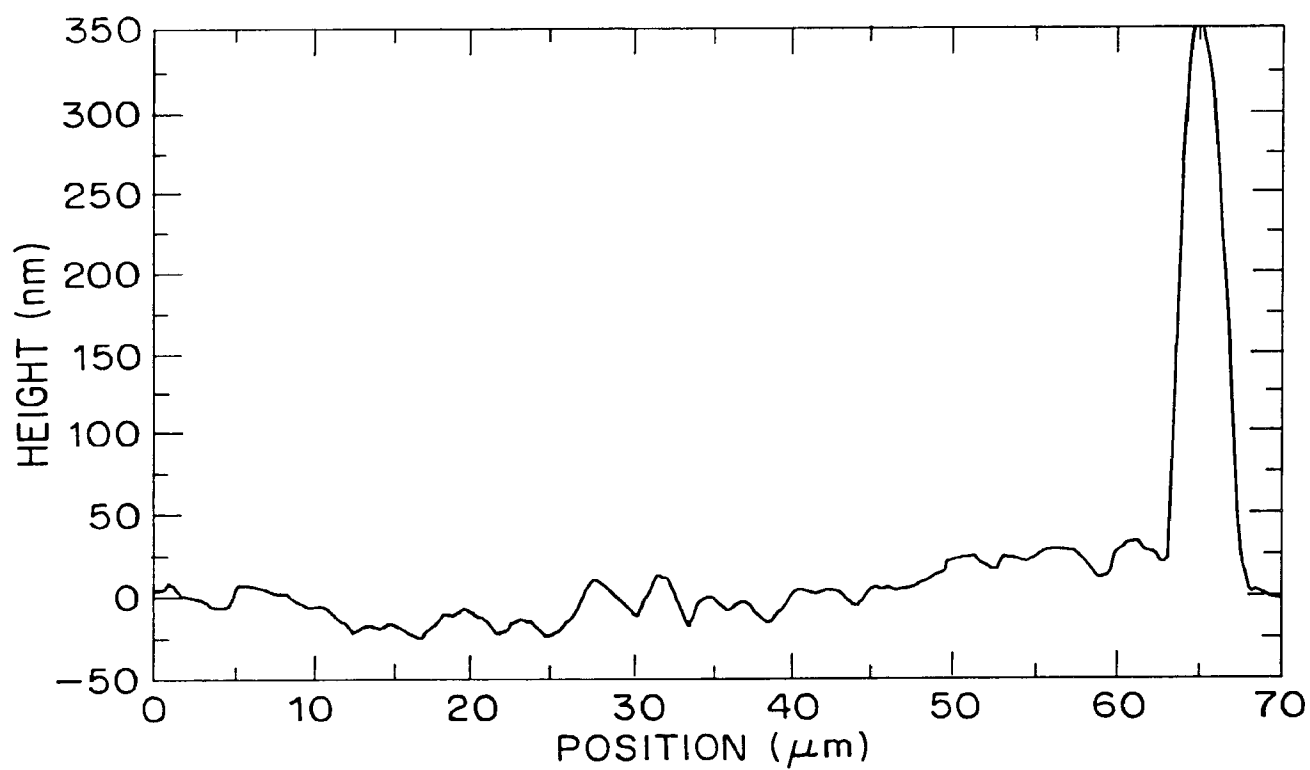


FIG. 2

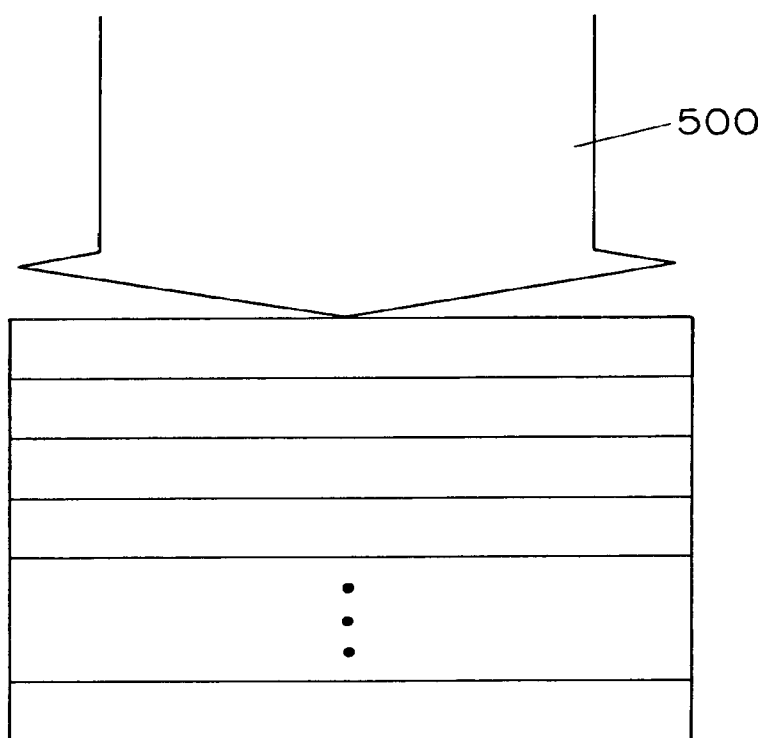


FIG. 5

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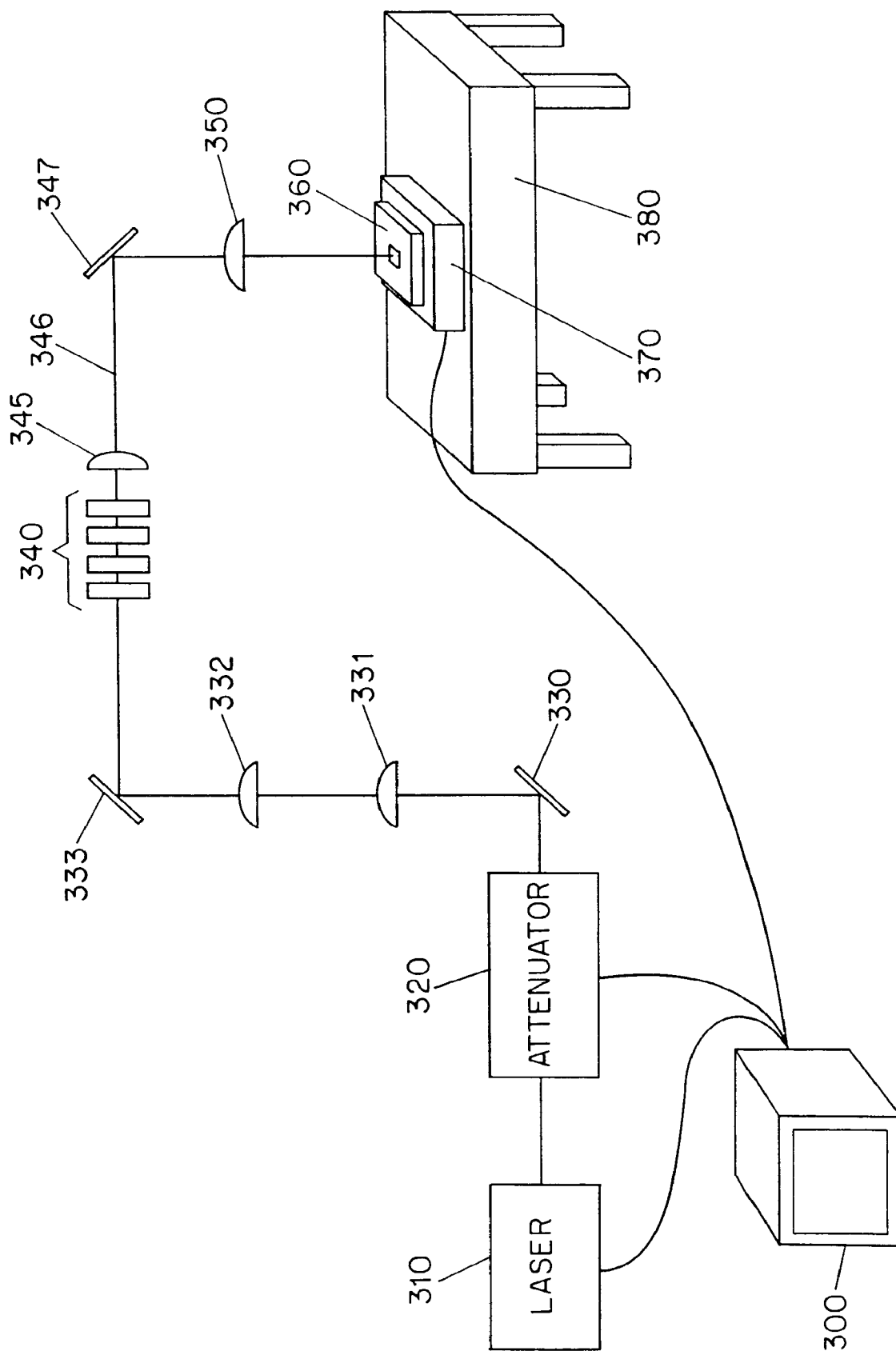


FIG. 3

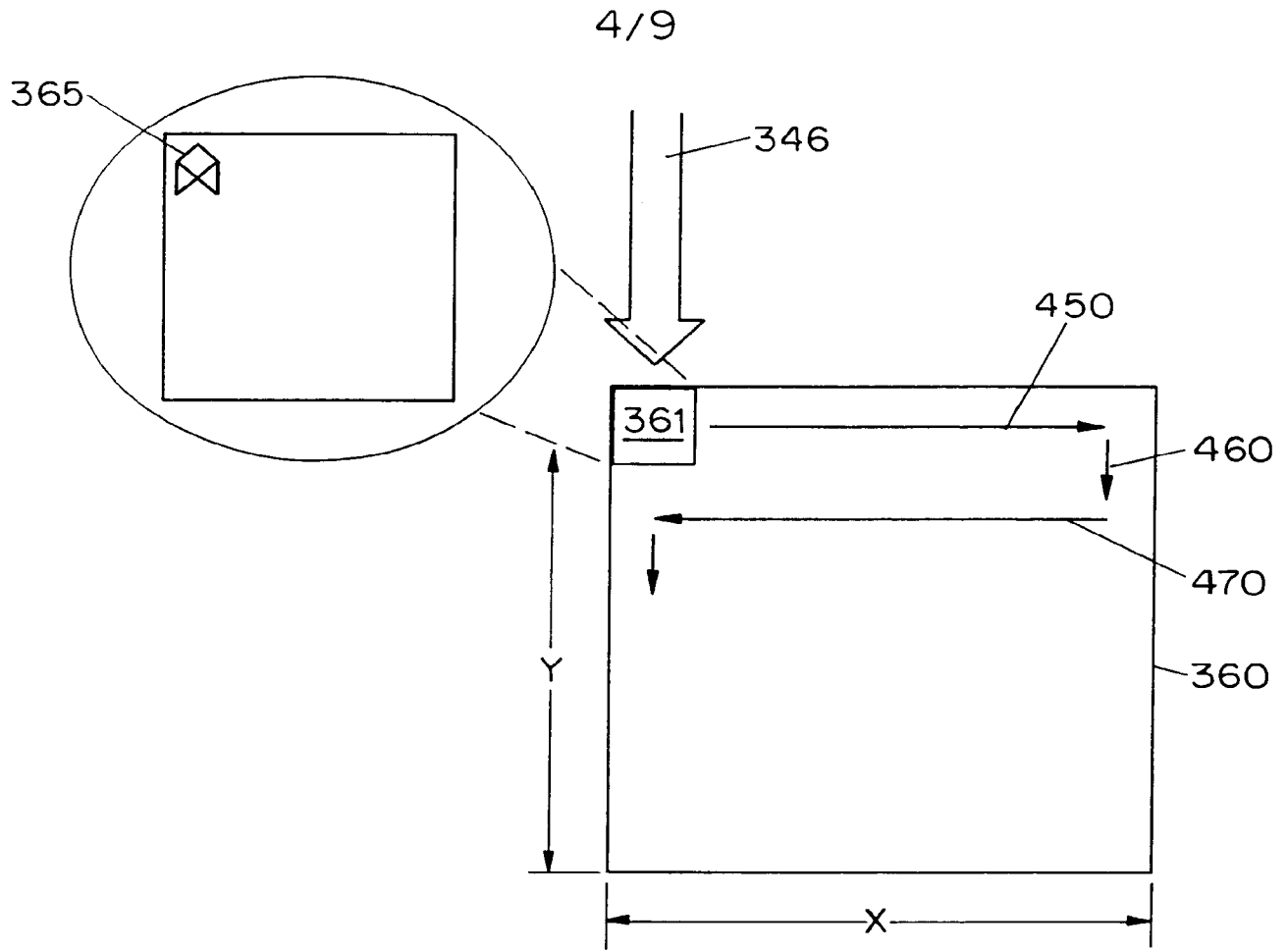


FIG. 4a

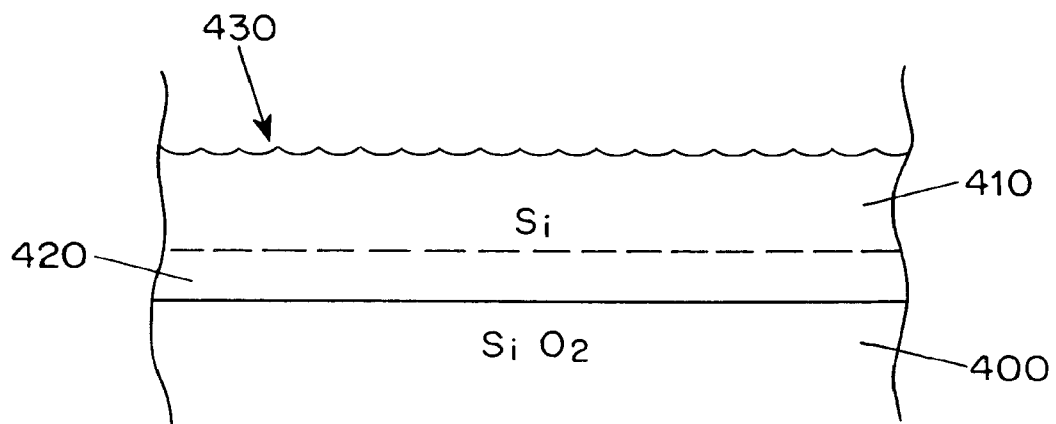


FIG. 4b

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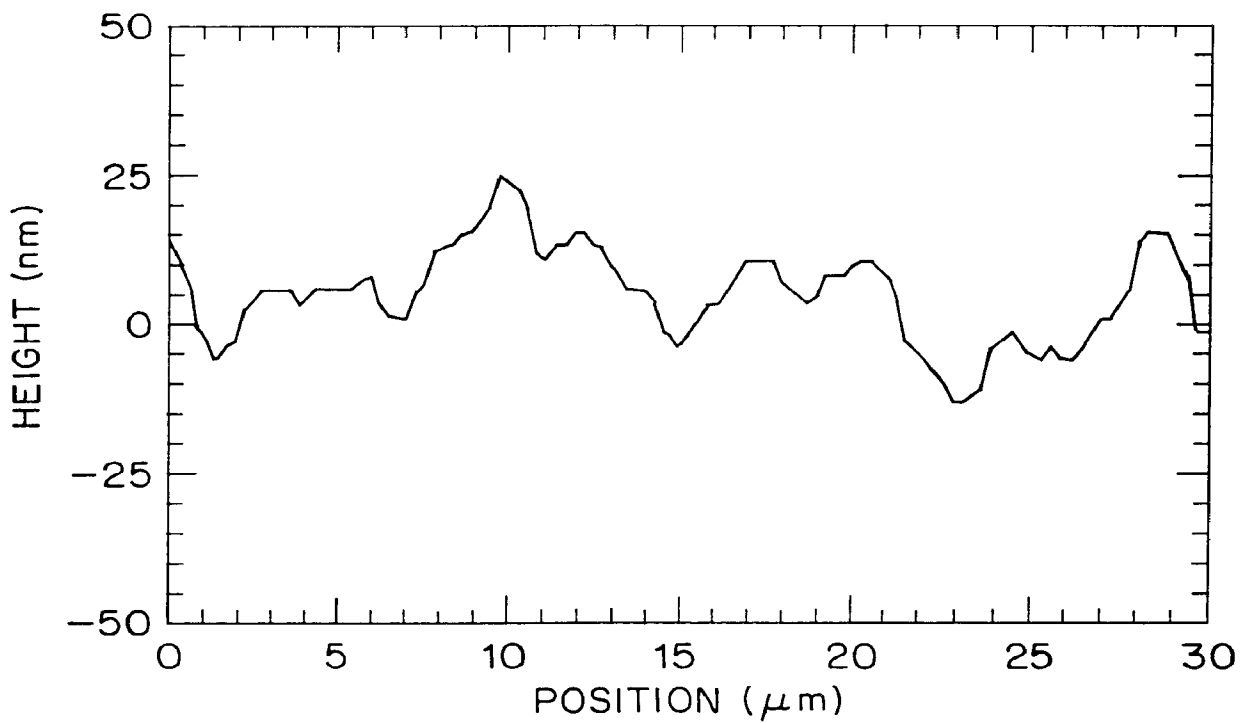


FIG. 6a

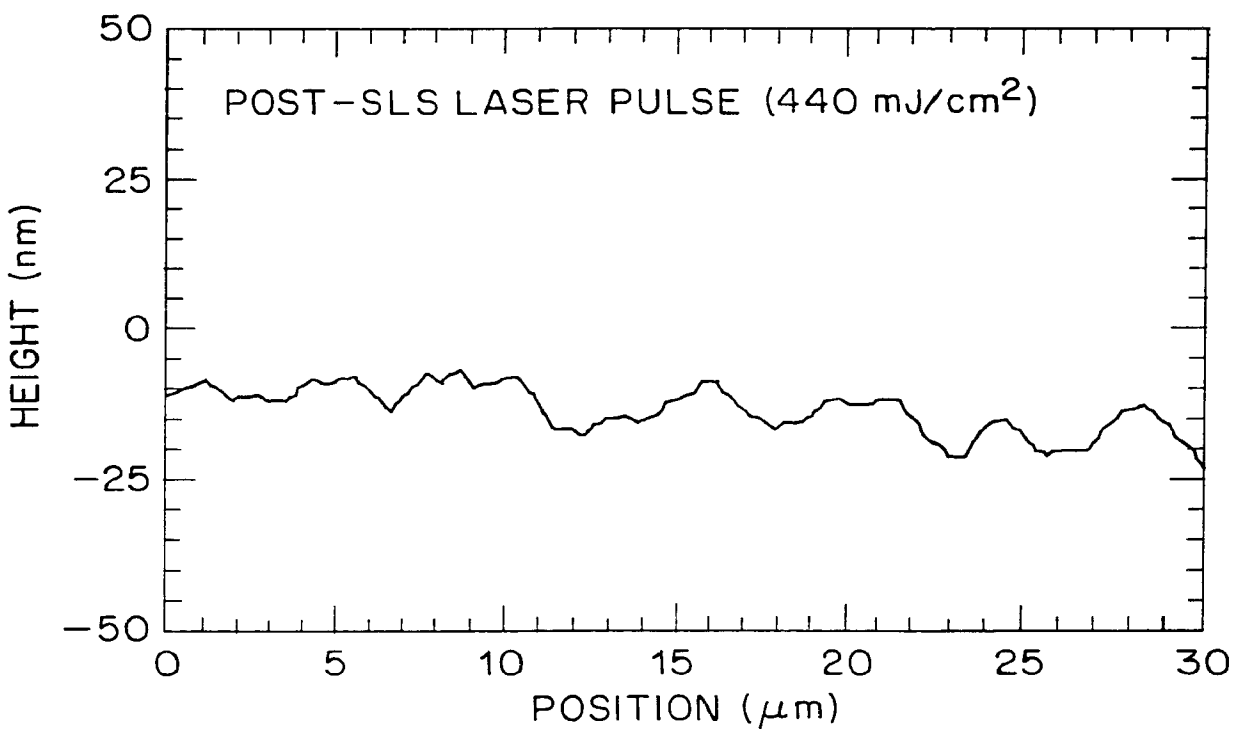


FIG. 6b

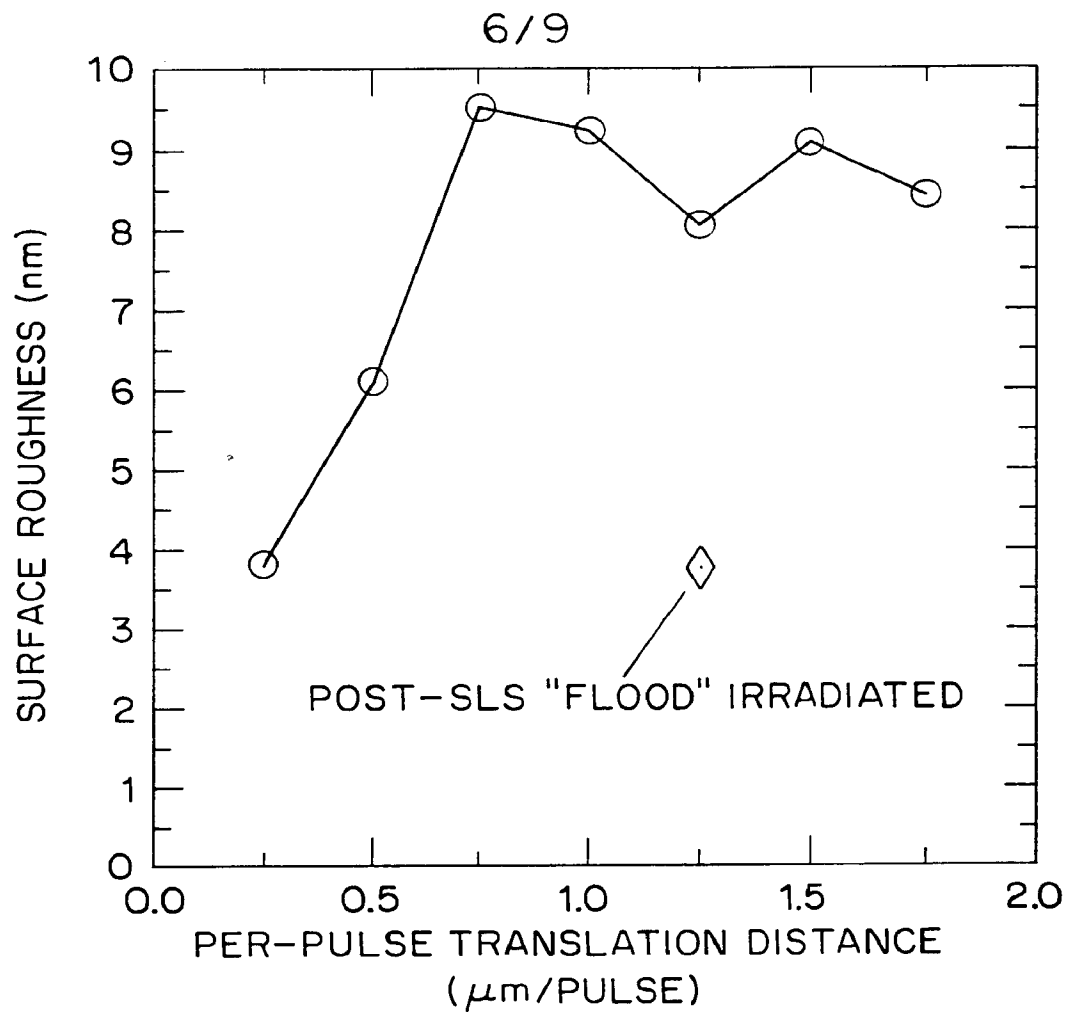


FIG. 7

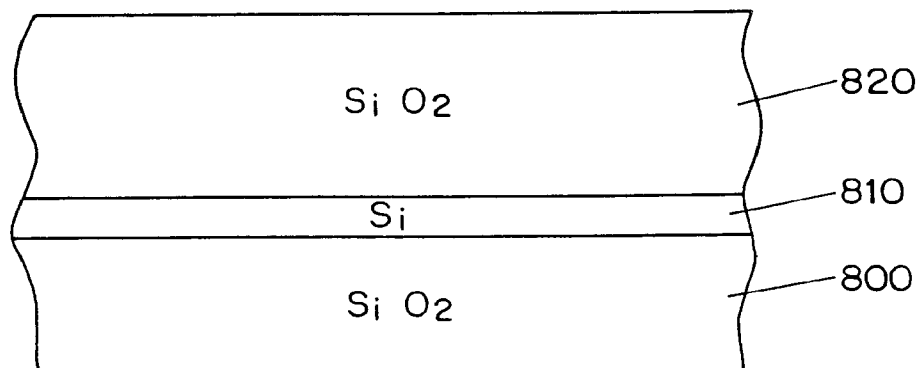


FIG. 8

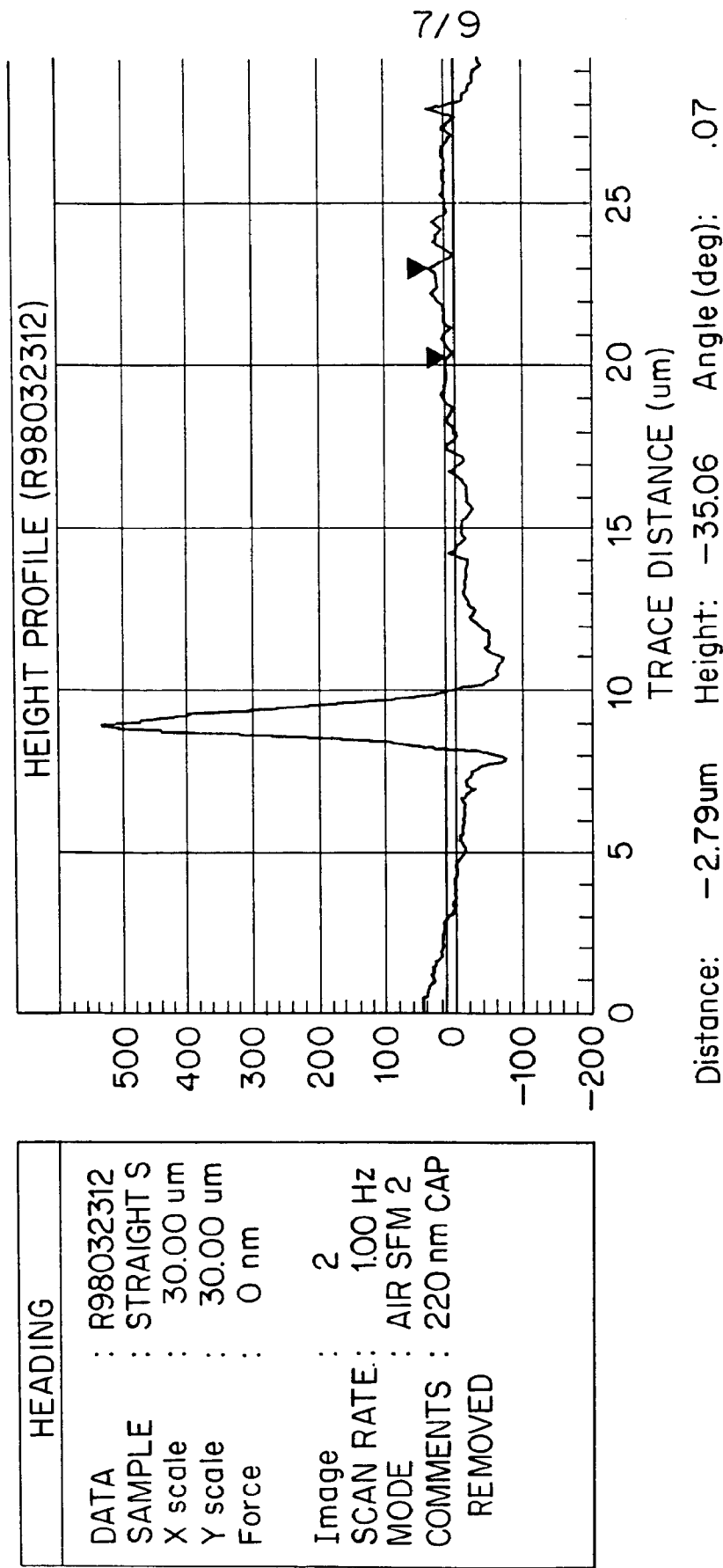


FIG. 9

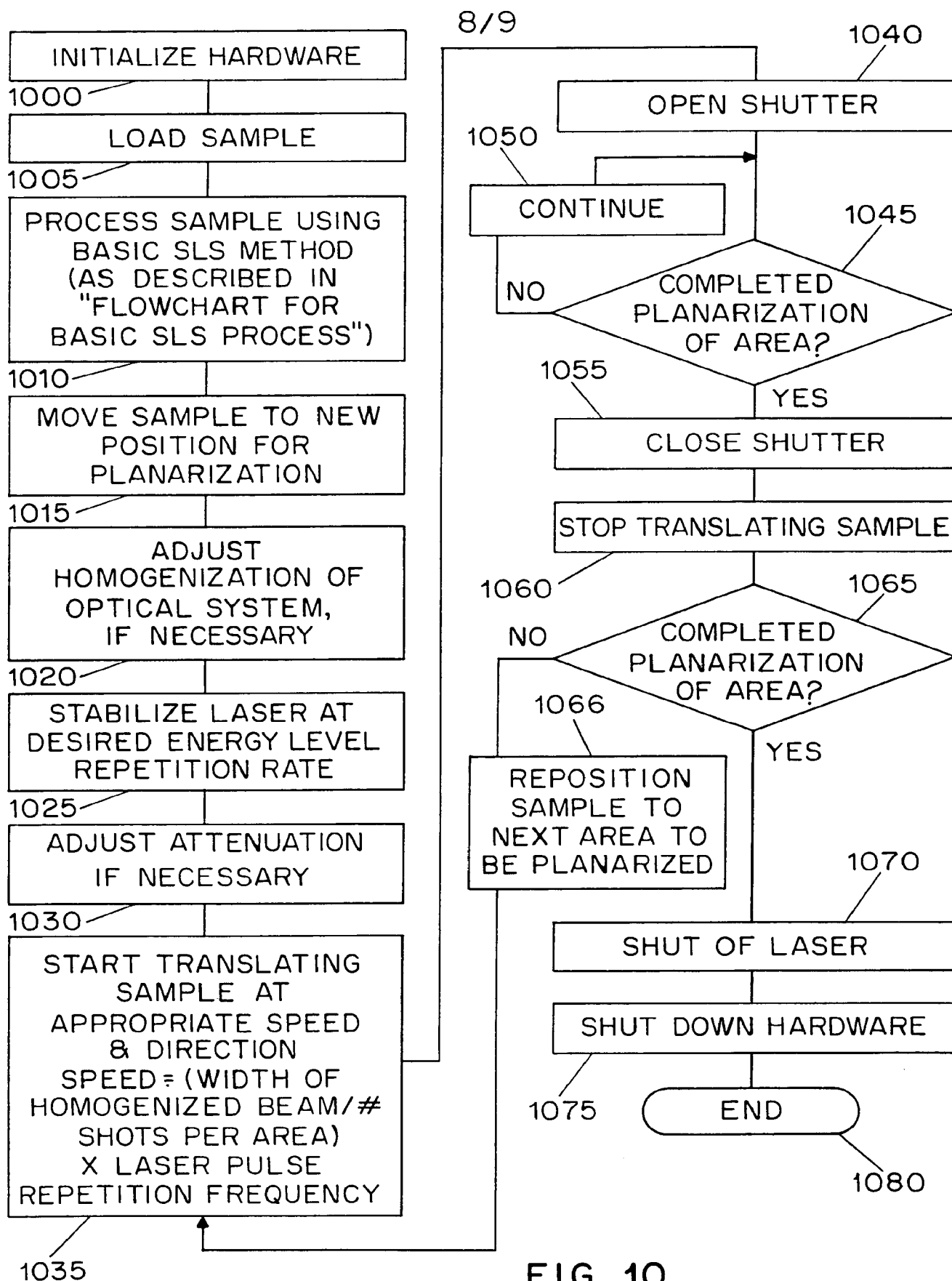


FIG. 10

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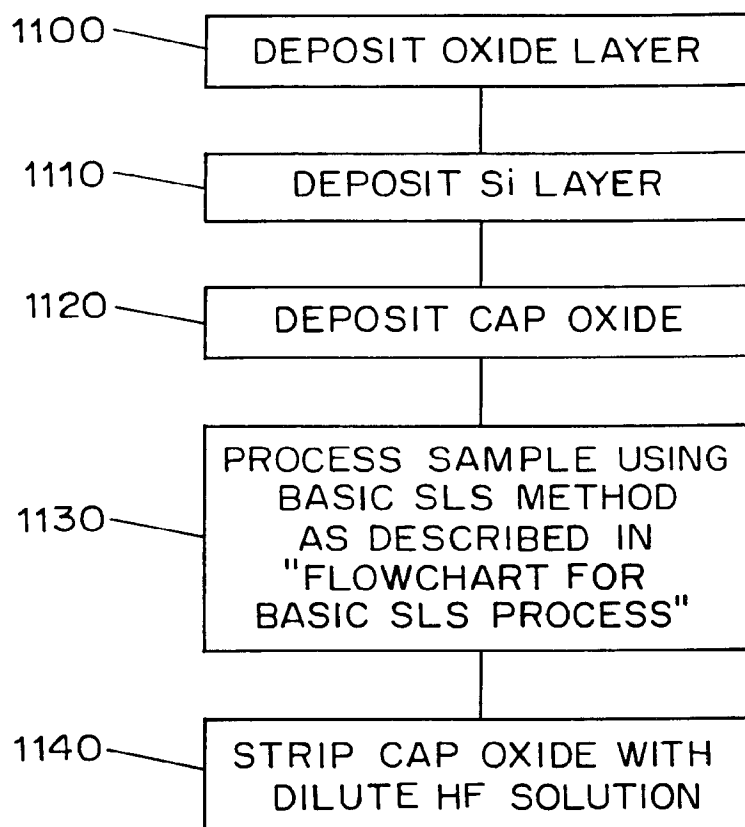


FIG. 11

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US00/07479

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) :H01L 21/31, 21/00, 21/26, 21/38
US CL :438/151, 163, 478, 482, 486, 487, 535, 555, 758
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 438/151, 163, 478, 482, 486, 487, 535, 555, 758

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
USPTO APS EAST

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,591,668 A (MAEGAWA et al.) 07 January 1997 (07.01.1997), the entire reference.	3-9, 11-18, 20-21
X	US 5,523,193 A (NELSON) 04 June 1996 (04.06.1996), the entire reference.	1-2, 5, 9-10, 13, 18-19



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
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"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

13 JUNE 2000

Date of mailing of the international search report

06 JUL 2000

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